

Cate Control Voltage

Vg

Steady

ramp-up/
ramp-down

Area

Outside Hi

7

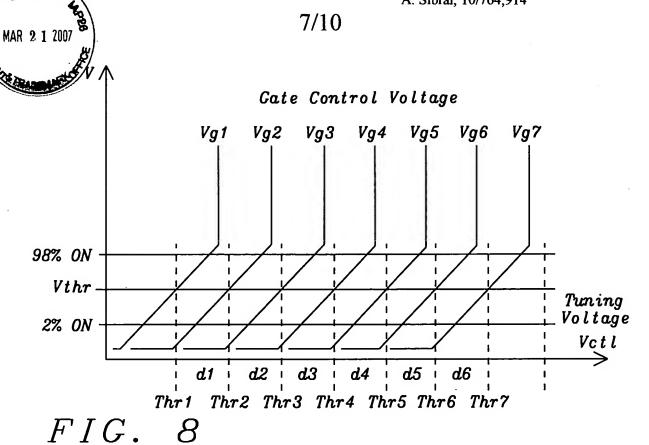
Voltage

CutOff Lo

CutOff Hi

Thr

FIG. 7b



Translinear Amplifier VDD C-MOS Switch Capacitor **VCO** ≷R1 Tr-Amp 1Vtune Sw 1 varCapCap 1 *₹R2* Tr-Amp 2Sw 2 Cap 2 ≩Rз Tr-Amp 3Sw 3 Cap 3 $\begin{cases} R_{n-1} \end{cases}$ Tr-Amp n-1Sw n-1 Cap^{n-1} R_n

Ro

C-Ref-Out

Sw n

FIG.

Cap n

8/10

